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Si/SiO₂.

Si/SiO₂.

Si/SiO₂ (~1 nm),

THE EFFECT OF CARRIER CAPTURE IN OXIDE ON THE CHARACTERISTICS OF MRI TRANSISTORS

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Abstract. *The paper investigates the effects in field-effect MRI transistors associated with the capture of charges by traps located both in the subcutaneous oxide layer and at the Si/SiO₂ interface. It is shown that the appearance of a positive charge is caused by the capture of holes by traps located near the boundary of the valence band of the anode, and at high stress doses, electron capture begins to play a predominant role. In the case of negative gate voltages, the positive charge in the oxide is located near the Si/SiO₂ interface (~1 nm), that is, within the limits of electron tunneling. The current through a thin oxide, resulting from the Fowler-Nordheim effect, leads to the accumulation of charges in the oxide layer, as well as to the formation of traps at the Si/SiO₂ interface.*

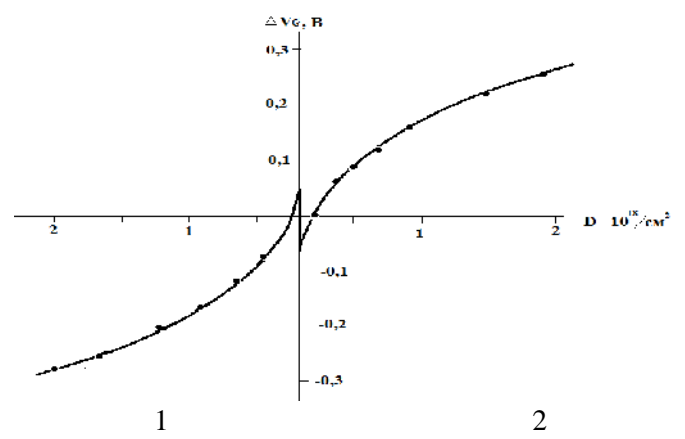
Keywords: gate oxide, tunnel capture, field effect transistor, threshold voltage drift, interface, stress dose, Fowler-Nordheim effect, degradation of characteristics, current density.

[1–6].

Si/SiO₂.

(100) $2 \times 10^{15} \text{ cm}^{-3}$ ($1,3 \times 10^{17} \text{ cm}^{-3}$)
 900 ° $10^{-1,3}$ N₂ $10^{-1,5}$ [7].
 (1). 20-
 450 ° .
 ()
 () 1
 [3].

. 1 (1) (2)
 1 cm^2 / $0,2 \text{ cm}^2$



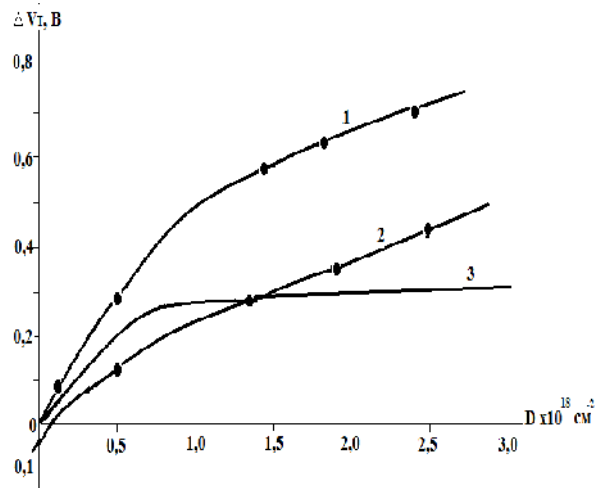
. 1. (1) (2) 1 cm^2

Si/SiO₂.

0,65

$\Delta V_{\bar{g}}^+$,
($\Delta V_T - \Delta V_{\bar{g}}^+$)

ΔV_T



.2.

ΔV_T (1)

, 2 -

$\Delta V_{\bar{g}}^+$, 3 -

($\Delta V_T - \Delta V_{\bar{g}}^+$)

(.3)

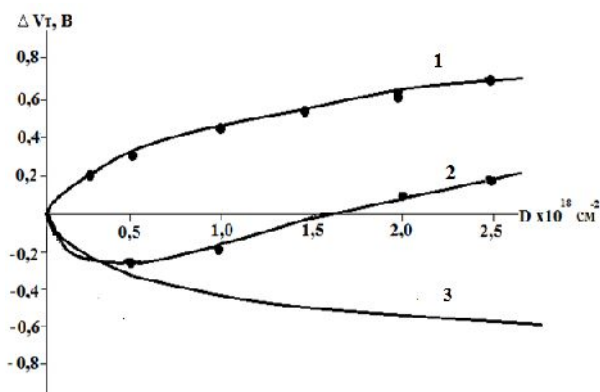
Si/SiO₂ (~1),

$\Delta V_{\bar{g}}^+$

I_d

V_g

Si/SiO₂ [3].



.3.

ΔV_T (2)

: 1 -

$\Delta V_{\bar{g}}^+$, 3 -

($\Delta V_T - \Delta V_{\bar{g}}^+$)

1. , - , Si/SiO₂.
2. , , 10¹⁷ -² -
3. Si/SiO₂
4. 0,65 - -
5. 10¹⁷ -² -

1. :
2. , 2011. 240 .
3. , 1984. 456 .
4. , 2011. 800 .

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